

DISPLAY PANEL FOR A DISPLAY DEVICE

Abstract of Disclosure

In each pixel region of a liquid crystal panel of an in plane switching mode, a pixel electrode is made to have a two-layer structure composed of a lower pixel electrode located in the same layer as a signal line and of an upper pixel electrode located in the same layer as a common electrode. Moreover, an electrode portion of the lower pixel electrode is made closer to the signal line than the upper pixel electrode. Furthermore, the signal line, a gate line and the common electrode are arranged in an overlapped manner.

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Figures

[illegible]